

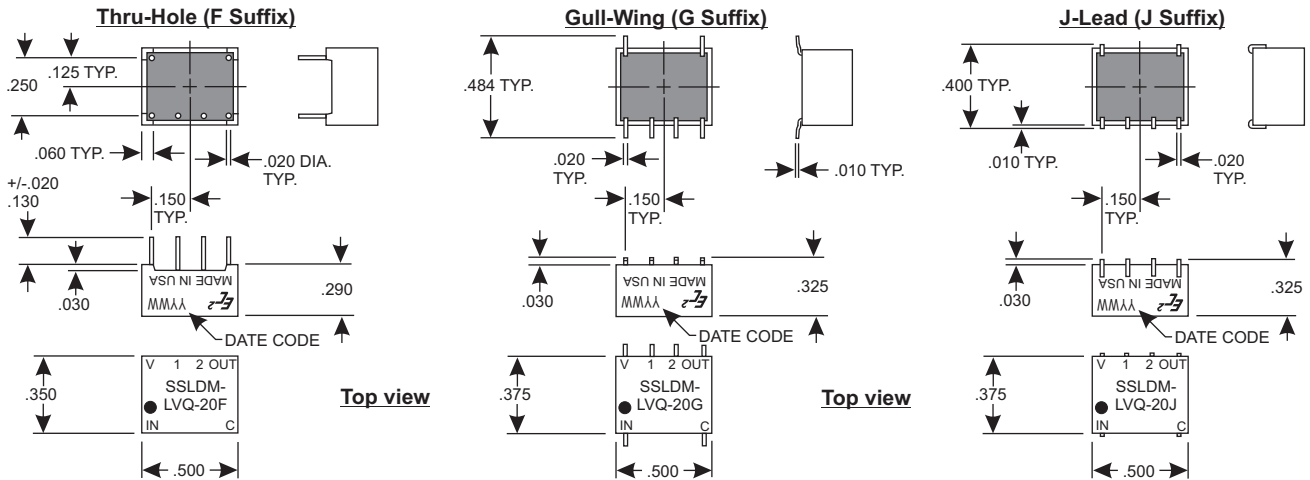
Space Saver LVQ 3V Logic Delay Module

The Space Saver LVQ 3V Logic Delay Modules manufactured by Engineered Components Company are designed to provide output waveforms that reproduce the input waveform after a set amount of delay time has elapsed. The three output waveforms are delay line taps provided at 33% increments of the total delay (33, 67, and 100%). These delay modules are non-inverting. The delay times are calibrated to the listed tolerances on the rising edge delays. The products with a total delay of less than 24ns have additional delay present at tap 1 due to internal propagation delays (see the Product Selection Table).

The MTBF on these modules, when calculated per MIL-HDBK-217, for a 50 deg.C ground fixed environment and with 50VDC applied, is in excess of 4 million hours. The temperature coefficient of delay is less than 1200 ppm/deg.C over the operating temperature range of -40 to +85 deg. C.

The module is provided in a 8-pin Space Saver package, fully encapsulated in epoxy resin and is housed in a Diallyl Phthalate case, blue in color. The case marking is applied by silkscreen using white epoxy paint. The 6 copper leads are tin-lead plated and meet the solderability requirements of MIL-STD-202, Method 208.

MECHANICAL DIAGRAM



Product Selection Table (Add F suffix for Thru-Hole Leads, G suffix for Gull-Wing Leads, or J suffix for J-Leads)

Part Number	Output Delay and Tolerances (in ns)		
	Tap 1 (33%)	Tap 2 (66%)	Tap 3 (100%)
SSLDM-LVQ-8	6.0+/-1.0	7.0+/-1.0	8.0+/-1.0
SSLDM-LVQ-9	6.0+/-1.0	7.5+/-1.0	9.0+/-1.0
SSLDM-LVQ-10	6.0+/-1.0	8.0+/-1.0	10.0+/-1.0
SSLDM-LVQ-12	6.0+/-1.0	9.0+/-1.0	12.0+/-1.0
SSLDM-LVQ-14	6.0+/-1.0	10.0+/-1.0	14.0+/-1.0
SSLDM-LVQ-16	6.0+/-1.0	11.0+/-1.0	16.0+/-1.0
SSLDM-LVQ-18	6.0+/-1.0	12.0+/-1.0	18.0+/-1.0
SSLDM-LVQ-20	6.0+/-1.0	13.0+/-1.0	20.0+/-1.0
SSLDM-LVQ-24	8.0+/-1.0	16.0+/-1.0	24.0+/-1.0
SSLDM-LVQ-27	9.0+/-1.0	18.0+/-1.0	27.0+/-1.0
SSLDM-LVQ-30	10.0+/-1.0	20.0+/-1.0	30.0+/-1.0
SSLDM-LVQ-36	12.0+/-1.0	24.0+/-1.0	36.0+/-1.0
SSLDM-LVQ-45	15.0+/-1.0	30.0+/-1.0	45.0+/-1.0
SSLDM-LVQ-60	20.0+/-1.0	40.0+/-1.0	60.0+/-1.0
SSLDM-LVQ-75	25.0+/-1.0	50.0+/-2.0	75.0+/-2.5
SSLDM-LVQ-90	30.0+/-1.5	60.0+/-2.5	90.0+/-3.0
SSLDM-LVQ-105	35.0+/-1.5	70.0+/-2.5	105.0+/-3.0
SSLDM-LVQ-120	40.0+/-1.5	80.0+/-3.0	120.0+/-4.0
SSLDM-LVQ-135	45.0+/-2.0	90.0+/-3.0	135.0+/-4.0
SSLDM-LVQ-150	50.0+/-2.0	100.0+/-3.0	150.0+/-5.0

Special modules can often be manufactured to provide for customer specific applications.

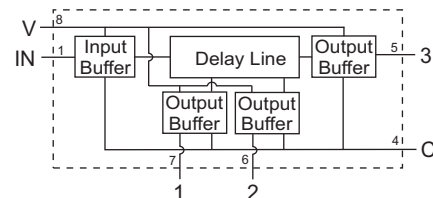
Operating Specifications:

All measurements made at 25 deg. C
 All measurements made with Vcc = +3.3VDC
 All measurements made with (1) LVQ output load

Operating Temperature: -40 to +85 deg. C
 Storage Temperature: -55 to +125 deg. C

Vcc Supply Voltage: 2.70 to 3.60VDC
 Vcc Supply Current:
 Constant "0" or "1" in = +/-1.0uA max.
 1MHz square wave in = 1mA typical
 Logic "High" Input:
 Voltage: 2.0VDC min. ; Vcc max.
 Current: Vcc max.; +/-1uA max.
 Logic "Low" Input:
 Voltage: 0.8 VDC max.
 Current: +/-1uA max.
 Logic "High" Voltage Out: 2.2VDC min.
 Logic "Low" Voltage Out: 0.4VDC max.

BLOCK DIAGRAM



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