

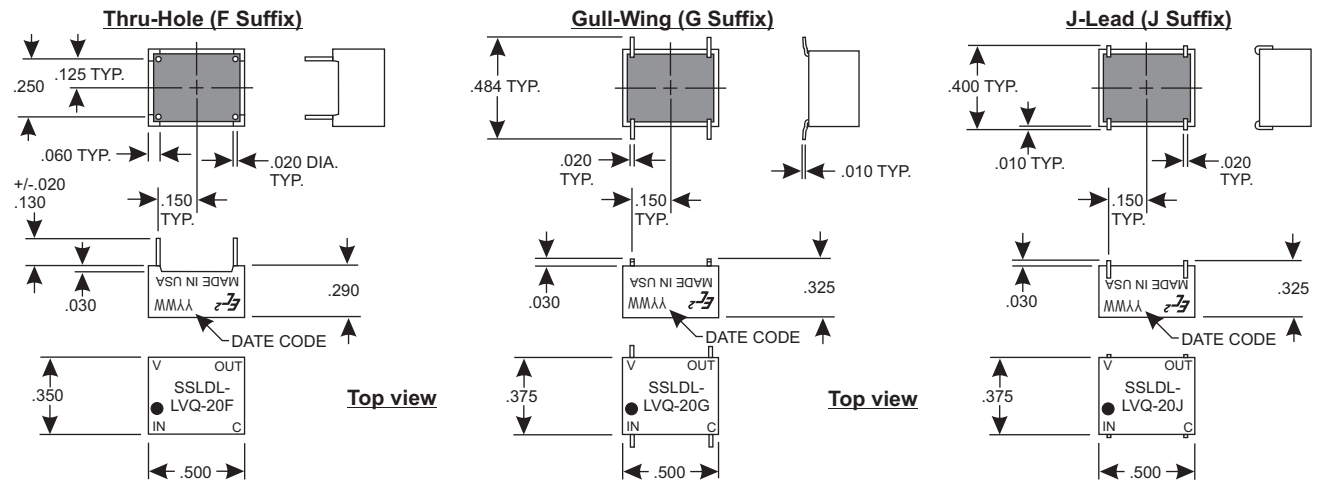
Space Saver LVQ 3V Logic Delay Line

The Space Saver LVQ 3V Logic Delay Lines manufactured by Engineered Components Company are designed to provide an output waveform that reproduces the input waveform after a set amount of delay time has elapsed. These delay lines are non-inverting. The delay times are calibrated to the listed tolerances on the rising edge delays.

The MTBF on these modules, when calculated per MIL-HDBK-217, for a 50 deg.C ground fixed environment and with 50VDC applied, is in excess of 4 million hours. The temperature coefficient of delay is less than 1200 ppm/deg.C over the operating temperature range of -40 to +85 deg. C.

The module is provided in a 8-pin Space Saver package, fully encapsulated in epoxy resin and is housed in a Diallyl Phthalate case, blue in color. The case marking is applied by silkscreen using white epoxy paint. The 4 copper leads are tin-lead plated and meet the solderability requirements of MIL-STD-202, Method 208.

MECHANICAL DIAGRAM



Product Selection Table

(Add Suffixes for Lead designation, F, G, or J)

Part Number	Output Delay and Tolerance (in ns)
SSLDL-LVQ-6	6.0+/-0.5
SSLDL-LVQ-7	7.0+/-0.5
SSLDL-LVQ-8	8.0+/-0.5
SSLDL-LVQ-9	9.0+/-0.5
SSLDL-LVQ-10	10.0+/-0.5
SSLDL-LVQ-11	11.0+/-0.75
SSLDL-LVQ-12	12.0+/-0.75
SSLDL-LVQ-13	13.0+/-0.75
SSLDL-LVQ-14	14.0+/-0.75
SSLDL-LVQ-15	15.0+/-0.75
SSLDL-LVQ-16	16.0+/-0.75
SSLDL-LVQ-17	17.0+/-0.75
SSLDL-LVQ-18	18.0+/-0.75
SSLDL-LVQ-19	19.0+/-0.75
SSLDL-LVQ-20	20.0+/-0.75
SSLDL-LVQ-21	21.0+/-1.0
SSLDL-LVQ-22	22.0+/-1.0
SSLDL-LVQ-23	23.0+/-1.0
SSLDL-LVQ-24	24.0+/-1.0

Product Selection Table (Cont.)

(Add Suffixes for Lead designation, F, G, or J)

Part Number	Output Delay and Tolerance (in ns)
SSLDL-LVQ-25	25.0+/-1.0
SSLDL-LVQ-30	30.0+/-1.5
SSLDL-LVQ-35	35.0+/-1.5
SSLDL-LVQ-40	40.0+/-1.5
SSLDL-LVQ-45	45.0+/-2.0
SSLDL-LVQ-50	50.0+/-2.0
SSLDL-LVQ-55	55.0+/-2.0
SSLDL-LVQ-60	60.0+/-2.0
SSLDL-LVQ-65	65.0+/-2.5
SSLDL-LVQ-70	70.0+/-2.5
SSLDL-LVQ-75	75.0+/-2.5
SSLDL-LVQ-80	80.0+/-2.5
SSLDL-LVQ-85	85.0+/-3.0
SSLDL-LVQ-90	90.0+/-3.0
SSLDL-LVQ-95	95.0+/-3.0
SSLDL-LVQ-100	100.0+/-3.0
SSLDL-LVQ-125	125.0+/-4.0
SSLDL-LVQ-150	150.0+/-4.5

Special modules can often be manufactured to provide for customer specific applications.

Operating Specifications:

All measurements made at 25 deg. C
 All measurements made with $V_{cc} = +3.3VDC$
 All measurements made with (1) LVQ output load

Operating Temperature: -40 to +85 deg. C
 Storage Temperature: -55 to +125 deg. C

V_{cc} Supply Voltage: 2.70 to 3.60VDC
 V_{cc} Supply Current:

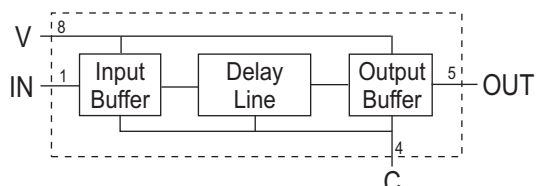
Constant "0" or "1" in = +/-1.0uA max.
 1MHz square wave in = 1mA typical

Logic "High" Input:
 Voltage: 2.0VDC min. ; V_{cc} max.
 Current: V_{cc} max. ; +/-1uA max.

Logic "Low" Input:
 Voltage: 0.8 VDC max.
 Current: +/-1uA max.

Logic "High" Voltage Out: 2.2VDC min.
 Logic "Low" Voltage Out: 0.4VDC max.

BLOCK DIAGRAM



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