

PART NUMBER TABLE

∅ DELAYS AND TOLERANCES (in ns)

Part Number	*Step Zero Delay Time	Maximum Delay Time (Nom)	Delay Change Per Step	**Maximum Deviation From Programmed Delay	- 3dB Bandwidth in Mhz
TADL-5.0-0.5	5.0 ± .5	8.5	0.5 ± .4	±0.5	50
TADL-7.5-0.5	7.5 ± .6	11.0	0.5 ± .4	±0.5	47
TADL-10.0-0.5	10.0 ± .7	13.5	0.5 ± .4	±0.5	44
TADL-12.5-0.5	12.5 ± .8	16.0	0.5 ± .4	±0.5	41
TADL-15.0-0.5	15.0 ± .9	18.5	0.5 ± .4	±0.5	38
TADL-17.5-0.5	17.5 ± 1.0	21.0	0.5 ± .4	±0.5	36
TADL-20.0-0.5	20.0 ± 1.1	23.5	0.5 ± .4	±0.5	34
TADL-22.5-0.5	22.5 ± 1.2	26.0	0.5 ± .4	±0.5	32
TADL-25.0-0.5	25.0 ± 1.3	28.5	0.5 ± .4	±0.5	30
TADL-27.5-0.5	27.5 ± 1.4	31.0	0.5 ± .4	±0.5	28
TADL-30-1.0	30 ± 1.5	37	1.0 ± .7	±1.0	26
TADL-35-1.0	35 ± 1.5	42	1.0 ± .7	±1.0	24
TADL-40-1.0	40 ± 2.0	47	1.0 ± .7	±1.0	23
TADL-45-1.0	45 ± 2.0	52	1.0 ± .7	±1.0	22
TADL-50-1.0	50 ± 2.5	57	1.0 ± .7	±1.0	21
TADL-55-1.0	55 ± 2.5	62	1.0 ± .7	±1.0	20
TADL-60-1.0	60 ± 2.5	67	1.0 ± .7	±1.0	19
TADL-65-1.0	65 ± 3.0	72	1.0 ± .7	±1.0	18
TADL-70-1.0	70 ± 3.0	77	1.0 ± .7	±1.0	18
TADL-75-1.0	75 ± 3.0	82	1.0 ± .7	±1.0	17
TADL-80-1.0	80 ± 3.0	87	1.0 ± .7	±1.0	17
TADL-85-1.0	85 ± 4.0	92	1.0 ± .7	±1.0	16
TADL-90-1.0	90 ± 4.0	97	1.0 ± .7	±1.0	15
TADL-95-1.0	95 ± 4.0	102	1.0 ± .7	±1.0	15
TADL-100-1.5	100 ± 4.0	110.5	1.5 ± 1.0	±1.5	14
TADL-110-1.5	110 ± 4.5	120.5	1.5 ± 1.0	±1.5	14
TADL-120-1.5	120 ± 4.5	130.5	1.5 ± 1.0	±1.5	13
TADL-130-1.5	130 ± 5.0	140.5	1.5 ± 1.0	±1.5	13
TADL-140-1.5	140 ± 5.0	150.5	1.5 ± 1.0	±1.5	12
TADL-150-1.5	150 ± 5.5	160.5	1.5 ± 1.0	±1.5	12
TADL-160-1.5	160 ± 5.5	170.5	1.5 ± 1.0	±1.5	11
TADL-170-1.5	170 ± 6.0	180.5	1.5 ± 1.0	±1.5	11
TADL-180-1.5	180 ± 6.5	190.5	1.5 ± 1.0	±1.5	10
TADL-190-1.5	190 ± 7.0	200.5	1.5 ± 1.0	±1.5	10
TADL-200-1.5	200 ± 7.5	210.5	1.5 ± 1.0	±1.5	9.0
TADL-210-1.5	210 ± 8.0	220.5	1.5 ± 1.0	±1.5	9.0
TADL-220-1.5	220 ± 8.5	230.5	1.5 ± 1.0	±1.5	8.0
TADL-230-1.5	230 ± 9.0	240.5	1.5 ± 1.0	±1.5	8.0
TADL-240-1.5	240 ± 9.5	250.5	1.5 ± 1.0	±1.5	7.0
TADL-250-1.5	250 ± 10.0	260.5	1.5 ± 1.0	±1.5	7.0

TRUTH TABLE EXAMPLES

Part Number	Programming Pins								
	3	0	0	0	0	1	1	1	1
	2	0	0	1	1	0	0	1	1
	1	0	1	0	1	0	1	0	1
TADL-10-0.5		10	.5	1	1.5	2	2.5	3	3.5
TADL-50-1.0		50	1	2	3	4	5	6	7
TADL-100-1.5		100	1.5	3	4.5	6	7.5	9	10.5
ETC.									

* Delay at step zero is referenced to the input pin.

**All delay times after step zero are referenced to step zero.

∅ Special modules can be readily manufactured to improve accuracies and/or provide customer specified delay times for specific applications.