

PART NUMBER TABLE

∅ DELAYS AND TOLERANCES (in ns)

Part Number	*Step Zero Delay Time	Maximum Delay Time (Nom)	Delay Change Per Step	**Maximum Deviation From Programmed Delay	- 3dB Bandwidth in Mhz
PADL-1-0.5	1.0 ± .5	4.5	0.5 ± .3	±0.4	45.0
PADL-1-1.0	1.0 ± .5	8.0	1.0 ± .3	±0.4	30.0
PADL-1-1.5	1.0 ± .5	11.5	1.5 ± .4	±0.5	25.0
PADL-1-2.0	1.0 ± .5	15.0	2.0 ± .4	±0.6	20.0
PADL-1-2.5	1.0 ± .5	18.5	2.5 ± .4	±0.7	17.0
PADL-1-3.0	1.0 ± .5	22.0	3.0 ± .5	±0.8	13.5
PADL-1-3.5	1.0 ± .5	25.5	3.5 ± .5	±0.9	12.0
PADL-1-4.0	1.0 ± .5	29.0	4.0 ± .5	±0.9	11.0
PADL-1-4.5	1.0 ± .5	32.5	4.5 ± .5	±1.0	10.5
PADL-1-5.0	1.0 ± .5	36.0	5.0 ± .5	±1.0	10.0

TRUTH TABLE EXAMPLES

Programming Part Number	Pins	3	0	0	0	0	1	1	1	1
		2	0	0	1	1	0	0	1	1
		1	0	1	0	1	0	1	0	1
PADL-1-0.5			1	.5	1	1.5	2	2.5	3	3.5
PADL-1-1.0			1	1	2	3	4	5	6	7
PADL-1-1.5			1	1.5	3	4.5	6	7.5	9	10.5
ETC.										

* Delay at step zero is referenced to the input pin.

**All delay times after step zero are referenced to step zero.

∅ Special modules can be readily manufactured to improve accuracies and/or provide customer specified delay times for specific applications.