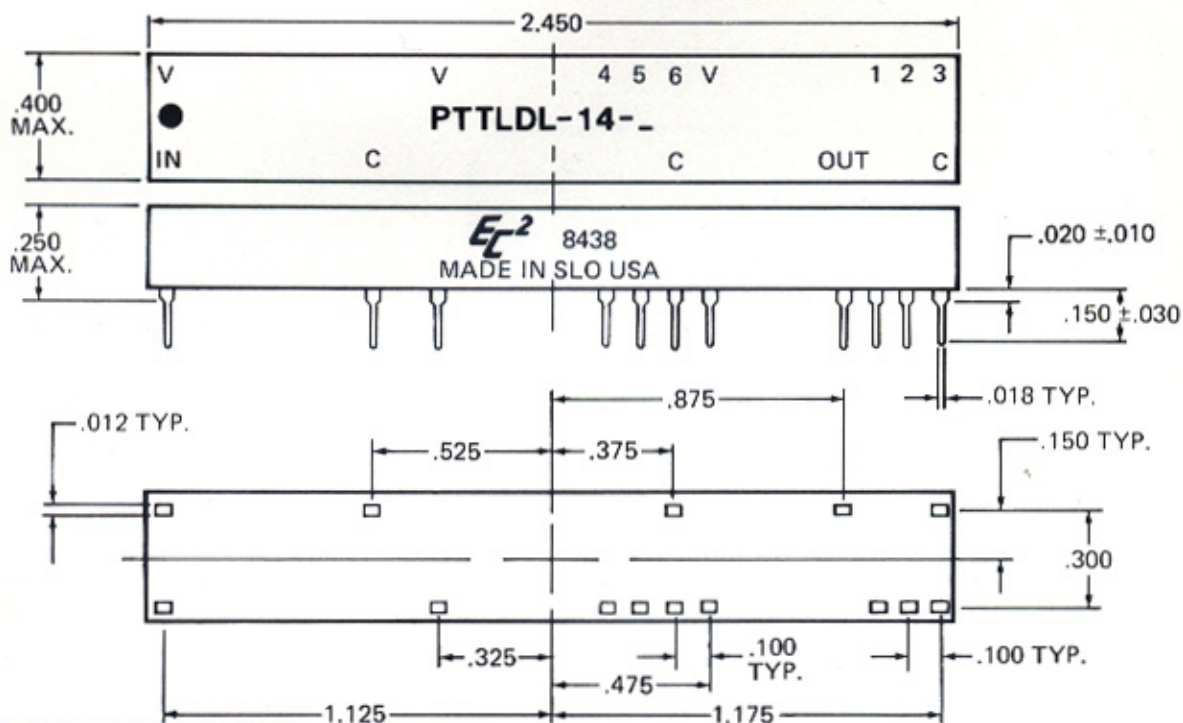


MECHANICAL DETAIL IS SHOWN BELOW



PART NUMBER TABLE

φ DELAYS AND TOLERANCES (in ns)				
Part Number	*Step Zero Delay Time	Maximum Delay Time (Nom)	Delay Change Per Step (Nom)	** Maximum Deviation From Programmed Delay
PTTLDL-14-1	14 ±.3	77	1	±1.5
PTTLDL-14-2	14 ±.3	140	2	±2
PTTLDL-14-3	14 ±.3	203	3	±3
PTTLDL-14-4	14 ±.3	266	4	±4
PTTLDL-14-5	14 ±.3	329	5	±5

TRUTH TABLE EXAMPLES

Part Number	Programming Pins															
	6	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1
PTTLDL-14-1	14	1	2	3	4	5	6	7	8	9	10	11	12	13	62	63
PTTLDL-14-2	14	2	4	6	8	10	12	14	16	18	20	22	24	26	124	126
PTTLDL-14-3	14	3	6	9	12	15	18	21	24	27	30	33	36	39	186	189
PTTLDL-14-4	14	4	8	12	16	20	24	28	32	36	40	44	48	52	248	252
PTTLDL-14-5	14	5	10	15	20	25	30	35	40	45	50	55	60	65	310	315

* Delay at step zero is referenced to the input pin.

** All delay times after step zero are referenced to step zero.

φ All modules can be operated with a minimum input pulse width of 25% of full delay and pulse period approaching square wave; since delay accuracies may be somewhat degraded, it is suggested that the module be evaluated under the intended specific operating conditions. Special modules can be readily manufactured to improve accuracies and/or provide customer specified delay times for specific applications.