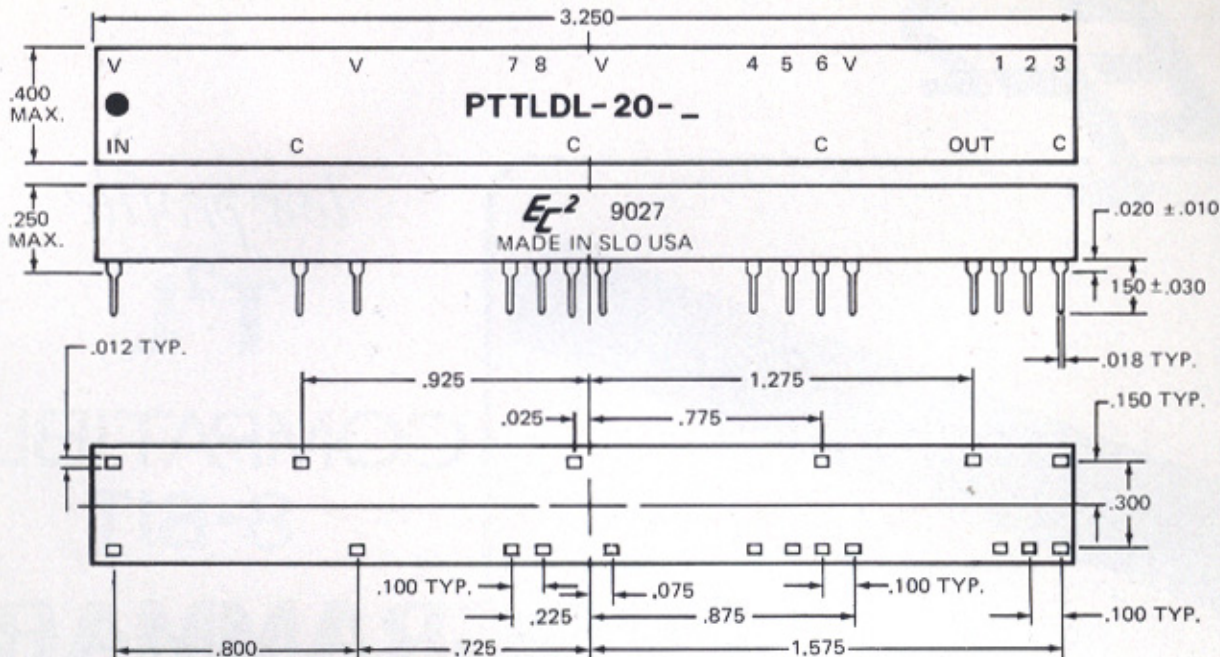


MECHANICAL DETAIL IS SHOWN BELOW



PART NUMBER TABLE

φ DELAYS AND TOLERANCES (in ns)				
Part Number	*Step Zero Delay Time	Maximum Delay Time (Nom)	Delay Change Per Step (Nom)	** Maximum Deviation From Programmed Delay
PTTLDL-20-1	20 ±.5	275	1	±6
PTTLDL-20-2	20 ±.5	530	2	±8
PTTLDL-20-3	20 ±.5	785	3	±12
PTTLDL-20-4	20 ±.5	1040	4	±16
PTTLDL-20-5	20 ±.5	1295	5	±20

TRUTH TABLE EXAMPLES

Part Number	Programming Pins																
	8	7	6	5	4	3	2	1	0	0	0	0	0	0	0	0	0
PTTLDL-20-1	20	1	2	3	4	5	6	7	8	9	10	11	12	13	254	255	
PTTLDL-20-2	20	2	4	6	8	10	12	14	16	18	20	22	24	26	508	510	
PTTLDL-20-3	20	3	6	9	12	15	18	21	24	27	30	33	36	39	762	765	
PTTLDL-20-4	20	4	8	12	16	20	24	28	32	36	40	44	48	52	1016	1020	
PTTLDL-20-5	20	5	10	15	20	25	30	35	40	45	50	55	60	65	1270	1275	

- * Delay at step zero is referenced to the input pin.
- ** All delay times after step zero are referenced to step zero.

φ All modules can be operated with a minimum input pulse width of 15% of full delay and pulse period approaching square wave; since delay accuracies may be somewhat degraded, it is suggested that the module be evaluated under the intended specific operating conditions. **Special modules can be readily manufactured to improve accuracies and/or provide customer specified delay times for specific applications.**