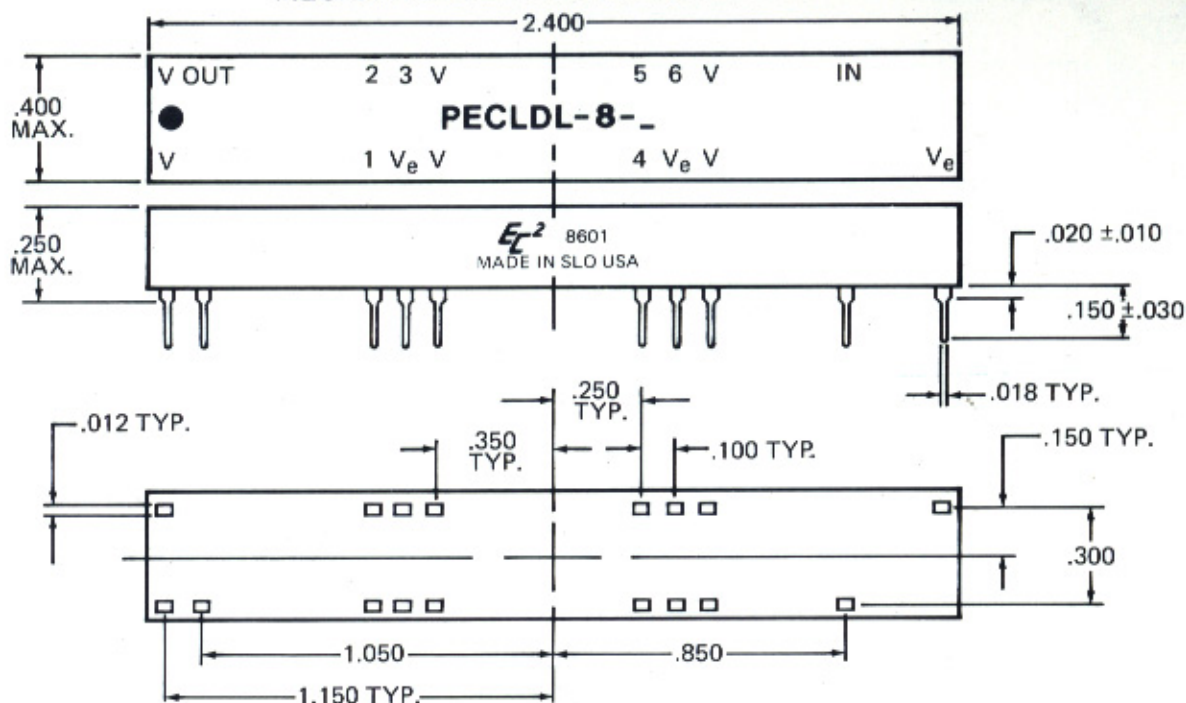


MECHANICAL DETAIL IS SHOWN BELOW



PART NUMBER TABLE

Ø DELAYS AND TOLERANCES (in ns)				
Part Number	*Step Zero Delay Time	Maximum Delay Time (Nom)	Delay Change Per Step (Nom)	** Maximum Deviation From Programmed Delay
PECLDL-8-1	8 ±.3	71	1	±1.5
PECLDL-8-2	8 ±.3	134	2	±2
PECLDL-8-3	8 ±.3	197	3	±3
PECLDL-8-4	8 ±.3	260	4	±4
PECLDL-8-5	8 ±.3	323	5	±5

TRUTH TABLE EXAMPLES

Part Number	Programming Pins														Output	
	6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
PECLDL-8-1	8	1	2	3	4	5	6	7	8	9	10	11	12	13	62	63
PECLDL-8-2	8	2	4	6	8	10	12	14	16	18	20	22	24	26	124	126
PECLDL-8-3	8	3	6	9	12	15	18	21	24	27	30	33	36	39	186	189
PECLDL-8-4	8	4	8	12	16	20	24	28	32	36	40	44	48	52	248	252
PECLDL-8-5	8	5	10	15	20	25	30	35	40	45	50	55	60	65	310	315

* Delay at step zero is referenced to the input pin.

**All delay times after step zero are referenced to step zero.

⊙ All modules can be operated with a minimum input pulse width of 25% of full delay and pulse period approaching square wave; since delay accuracies may be somewhat degraded, it is suggested that the module be evaluated under the intended specific operating conditions. [Special modules can be readily manufactured to improve accuracies and/or provide customer specified delay times for specific applications.](#)