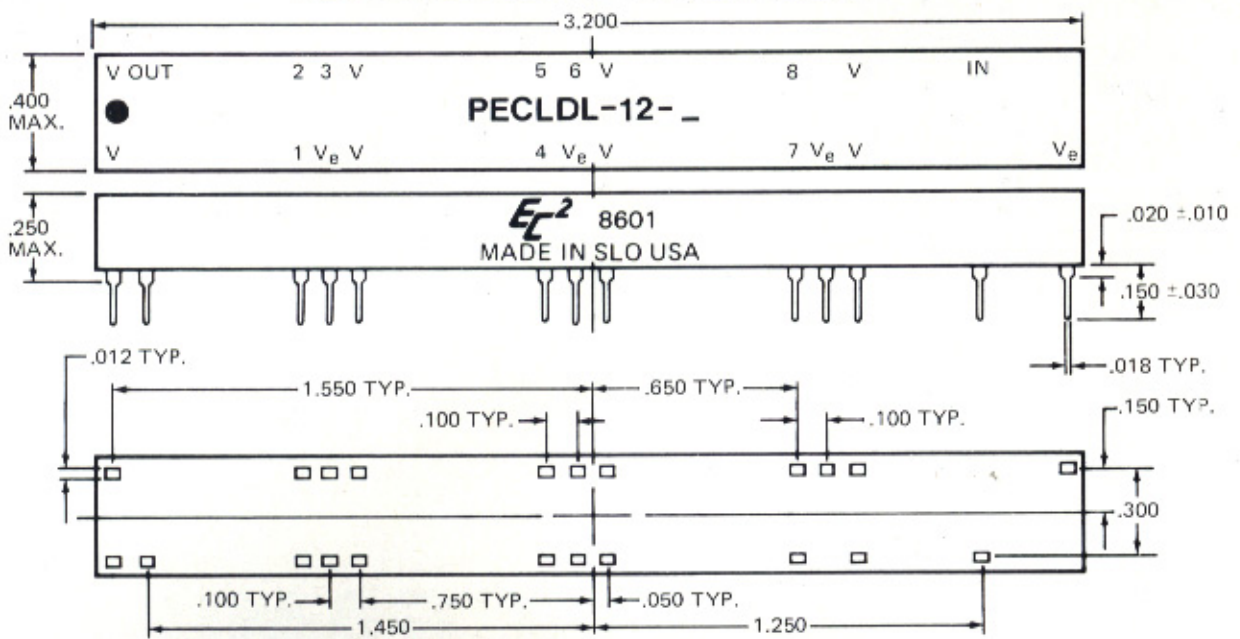


MECHANICAL DETAIL IS SHOWN BELOW



PART NUMBER TABLE

φ DELAYS AND TOLERANCES (in ns)				
Part Number	*Step Zero Delay Time	Maximum Delay Time (Nom)	Delay Change Per Step (Nom)	** Maximum Deviation From Programmed Delay
PECLDL-12-1	12 ±.5	267	1	±6
PECLDL-12-2	12 ±.5	522	2	±8
PECLDL-12-3	12 ±.5	777	3	±12
PECLDL-12-4	12 ±.5	1032	4	±16
PECLDL-12-5	12 ±.5	1287	5	±20

TRUTH TABLE EXAMPLES

Part Number	Programming Pins														Output	
	8	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
PECLDL-12-1	12	1	2	3	4	5	6	7	8	9	10	11	12	13	254	255
PECLDL-12-2	12	2	4	6	8	10	12	14	16	18	20	22	24	26	508	510
PECLDL-12-3	12	3	6	9	12	15	18	21	24	27	30	33	36	39	762	765
PECLDL-12-4	12	4	8	12	16	20	24	28	32	36	40	44	48	52	1016	1020
PECLDL-12-5	12	5	10	15	20	25	30	35	40	45	50	55	60	65	1270	1275

* Delay at step zero is referenced to the input pin.

** All delay times after step zero are referenced to step zero.

φ All modules can be operated with a minimum input pulse width of 15% of full delay and pulse period approaching square wave; since delay accuracies may be somewhat degraded, it is suggested that the module be evaluated under the intended specific operating conditions. **Special modules can be readily manufactured to improve accuracies and/or provide customer specified delay times for specific applications.**