

PART NUMBER TABLE

BINARY ϕ DELAYS AND TOLERANCES (in ns)				
Part Number	*Step Zero Delay Time	Maximum Delay Time (Nom)	**Maximum Deviaton From Programmed Delay (4LSB)	**Maximum Deviation From Programmed Delay (4MSB)
BPECLDL-4.5-0.1	4.5 \pm .3	30.0	\pm .10	\pm 1.0
BPECLDL-4.5-0.2	4.5 \pm .3	55.5	\pm .20	\pm 2.0
BPECLDL-4.5-0.3	4.5 \pm .3	81.0	\pm .25	\pm 4.0
BPECLDL-4.5-0.4	4.5 \pm .3	106.5	\pm .30	\pm 5.0
BPECLDL-4.5-0.5	4.5 \pm .3	132.0	\pm .35	\pm 6.0

TRUTH TABLE EXAMPLES

Part Number \ Programming Pins	8	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1
	7	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1
	6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1
	5	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1
	4	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	3	0	0	0	0	1	1	1	1	1	0	0	0	0	1	1	1	1
	2	0	0	1	1	0	0	1	1	0	0	1	1	0	0	1	1	1
	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	0	1
BPECLDL-4.5-0.1	4.5	0.1	0.2	0.3	0.4	0.5	0.6	0.7	0.8	0.9	1.0	1.1	1.2	1.3	25.4	25.5		
BPECLDL-4.5-0.2	4.5	0.2	0.4	0.6	0.8	1.0	1.2	1.4	1.6	1.8	2.0	2.2	2.4	2.6	50.8	51.0		
BPECLDL-4.5-0.3	4.5	0.3	0.6	0.9	1.2	1.5	1.8	2.1	2.4	2.7	3.0	3.3	3.6	3.9	76.2	76.5		
BPECLDL-4.5-0.4	4.5	0.4	0.8	1.2	1.6	2.0	2.4	2.8	3.2	3.6	4.0	4.4	4.8	5.2	101.6	102.0		
BPECLDL-4.5-0.5	4.5	0.5	1.0	1.5	2.0	2.5	3.0	3.5	4.0	4.5	5.0	5.5	6.0	6.5	127.0	127.5		

PART NUMBER TABLE

DECADE ϕ DELAYS AND TOLERANCES (in ns)				
Part Number	*Step Zero Delay Time	Maximum Delay Time (Nom)	**Maximum Deviaton From Programmed Delay (Fine Decade)	**Maximum Deviation From Programmed Delay (Course Decade)
DPECLDL-4.5-0.1	4.5 \pm .3	14.4	\pm .10	\pm 0.5
DPECLDL-4.5-0.2	4.5 \pm .3	24.3	\pm .20	\pm 1.0
DPECLDL-4.5-0.3	4.5 \pm .3	34.2	\pm .25	\pm 1.5
DPECLDL-4.5-0.4	4.5 \pm .3	44.1	\pm .30	\pm 2.0
DPECLDL-4.5-0.5	4.5 \pm .3	54.0	\pm .35	\pm 2.5

TRUTH TABLE EXAMPLES

Part Number \ Programming Pins	8	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	
	7	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	5	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1
	4	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	1	1	1
	3	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	2	0	0	1	1	0	0	1	1	0	0	0	0	0	1	1	1	1	1
	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1
DPECLDL-4.5-0.1	4.5	0.1	0.2	0.3	0.4	0.5	0.6	0.7	0.8	0.9	1.0	1.1	1.2	1.3	9.8	9.9			
DPECLDL-4.5-0.2	4.5	0.2	0.4	0.6	0.8	1.0	1.2	1.4	1.6	1.8	2.0	2.2	2.4	2.6	19.6	19.8			
DPECLDL-4.5-0.3	4.5	0.3	0.6	0.9	1.2	1.5	1.8	2.1	2.4	2.7	3.0	3.3	3.6	3.9	29.4	29.7			
DPECLDL-4.5-0.4	4.5	0.4	0.8	1.2	1.6	2.0	2.4	2.8	3.2	3.6	4.0	4.4	4.8	5.2	39.2	39.6			
DPECLDL-4.5-0.5	4.5	0.5	1.0	1.5	2.0	2.5	3.0	3.5	4.0	4.5	5.0	5.5	6.0	6.5	49.0	49.5			

* Delay at step zero is referenced to the input pin.

** All delay times after step zero are referenced to step zero.

ϕ All modules can be operated with a minimum input pulse width of 20% of full delay, and pulse period approaching square wave; since delay accuracies may be somewhat degraded, it is suggested that the module be evaluated under the intended specific operating conditions. **Special modules can be readily manufactured to improve accuracies and/or provide customer specified delay times for specific applications.**