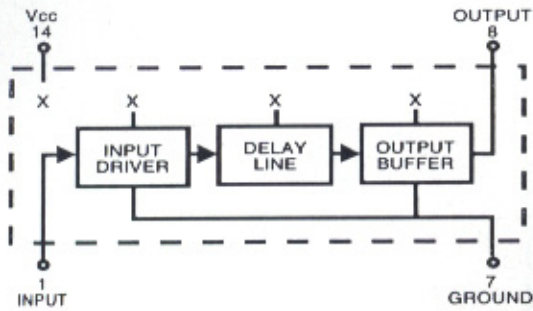
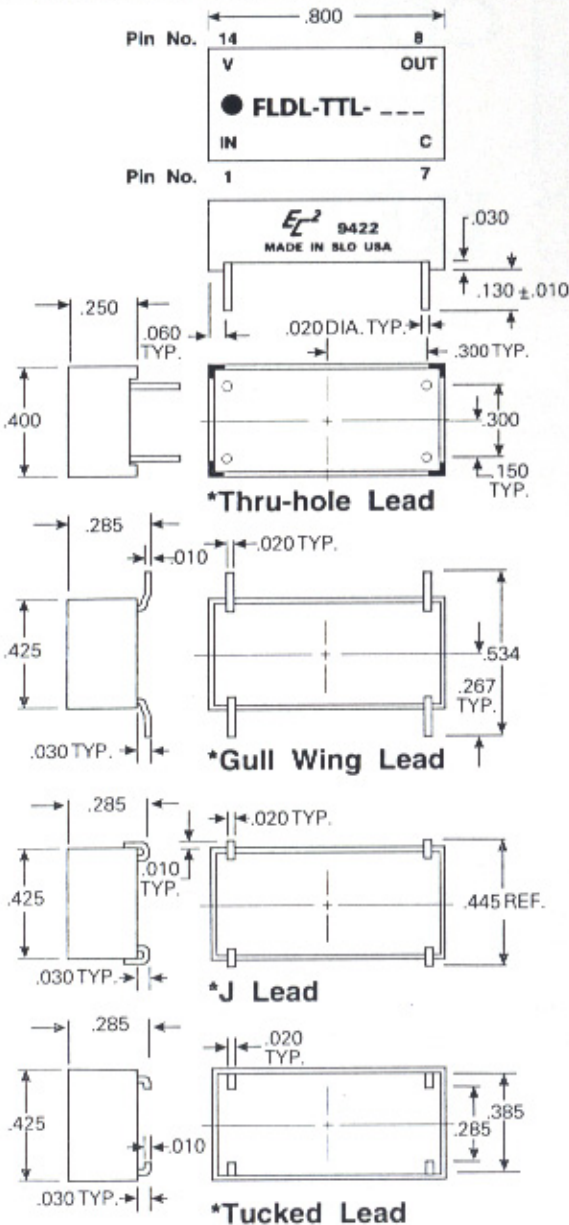


BLOCK DIAGRAM IS SHOWN BELOW



MECHANICAL DETAIL IS SHOWN BELOW



OPERATING SPECIFICATIONS

V_{CC} supply voltage: 4.75 to 5.25V DC
 V_{CC} supply current:
 Constant "0" in 40mA typical
 Constant "1" in 7mA typical

Logic 1 Input:
 Voltage 2V min.; V_{CC} max.
 Current 2.7V = 20uA max.
 5.5V = 1mA max.

Logic 0 Input:
 Voltage8V max.
 Current -6mA max.

Logic 1 Voltage out: 2.7V min.
 Logic 0 Voltage out:5V max.
 Operating temperature range: 0 to +70°C.
 Storage temperature: -55 to +125°C.

Delays increase or decrease approximately 4% for a respective increase or decrease of 5% in supply voltage.

PART NUMBER TABLE

* Suffix Part Number with G (for Gull Wing Lead), J (for J Lead), F (for Thru-hole Lead) or T (for Tucked Lead).
 Examples: FDL-TTL-10G (Gull Wing), FDL-TTL-25J (J Lead), FDL-TTL-75F (Thru-hole Lead) or FDL-TTL-80T (Tucked Lead)

DELAYS AND TOLERANCES (in ns)			
PART NO.	OUTPUT	PART NO.	OUTPUT
FDL-TTL-5	5 ± 1	FDL-TTL-60	60 ± 2
FDL-TTL-6	6 ± 1	FDL-TTL-65	65 ± 2.5
FDL-TTL-7	7 ± 1	FDL-TTL-70	70 ± 2.5
FDL-TTL-8	8 ± 1	FDL-TTL-75	75 ± 2.5
FDL-TTL-9	9 ± 1	FDL-TTL-80	80 ± 2.5
FDL-TTL-10	10 ± 1	FDL-TTL-85	85 ± 3
FDL-TTL-11	11 ± 1	FDL-TTL-90	90 ± 3
FDL-TTL-12	12 ± 1	FDL-TTL-95	95 ± 3
FDL-TTL-13	13 ± 1	FDL-TTL-100	100 ± 3
FDL-TTL-14	14 ± 1	FDL-TTL-125	125 ± 4
FDL-TTL-15	15 ± 1	FDL-TTL-150	150 ± 4.5
FDL-TTL-16	16 ± 1	FDL-TTL-175	175 ± 5
FDL-TTL-17	17 ± 1	FDL-TTL-200	200 ± 6
FDL-TTL-18	18 ± 1	FDL-TTL-225	225 ± 7
FDL-TTL-19	19 ± 1	FDL-TTL-250	250 ± 8
FDL-TTL-20	20 ± 1	FDL-TTL-275	275 ± 9
FDL-TTL-21	21 ± 1	FDL-TTL-300	300 ± 10
FDL-TTL-22	22 ± 1	FDL-TTL-350	350 ± 11
FDL-TTL-23	23 ± 1	FDL-TTL-400	400 ± 12
FDL-TTL-24	24 ± 1	FDL-TTL-450	450 ± 14
FDL-TTL-25	25 ± 1	FDL-TTL-500	500 ± 15
FDL-TTL-30	30 ± 1.5	FDL-TTL-600	600 ± 18
FDL-TTL-35	35 ± 1.5	FDL-TTL-700	700 ± 20
FDL-TTL-40	40 ± 1.5	FDL-TTL-800	800 ± 22
FDL-TTL-45	45 ± 2	FDL-TTL-900	900 ± 24
FDL-TTL-50	50 ± 2	FDL-TTL-1000	1000 ± 26
FDL-TTL-55	55 ± 2		

TEST CONDITIONS

- All measurements are made at 25°C.
- V_{CC} supply voltage is maintained at 5.0V DC.
- All units are tested using a FAST toggle-type positive input pulse and one FAST T²L load at the output.
- Input pulse width used is 100% longer than delay of module under test; spacing between pulses (falling edge to rising edge) is three times the pulse width used.

φ All modules can be operated with a minimum input pulse width of 100% of full delay and pulse period approaching square wave; since delay accuracies may be somewhat degraded, it is suggested that the module be evaluated under the intended specific operating conditions. **Special modules can be readily manufactured to improve accuracies and/or provide customer specified random delay times for specific applications.**