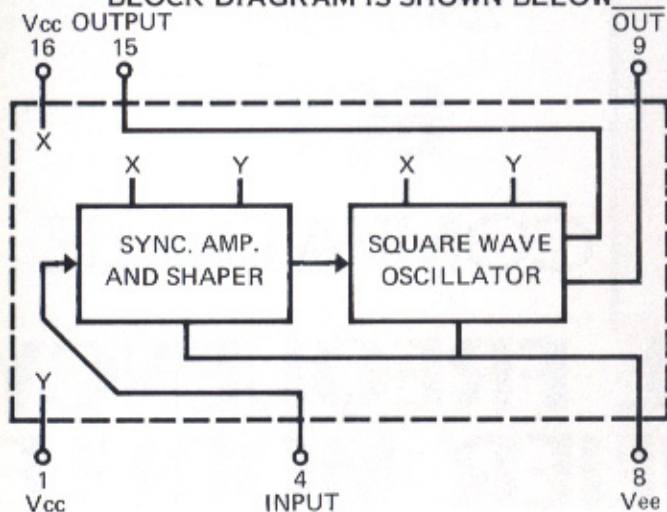


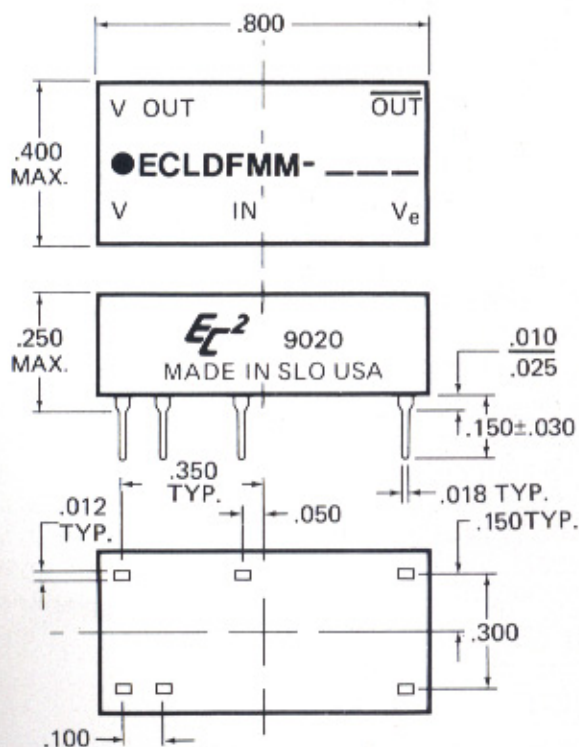
## DESIGN NOTES (continued)

Marking consists of manufacturer's name, logo (EC<sup>2</sup>), part number, terminal identification and date code of manufacture. All marking is applied by silk screen process using white epoxy paint in accordance with MIL-STD-130, to meet the permanency of identification required by MIL-STD-202, Method 215.

### BLOCK DIAGRAM IS SHOWN BELOW



### MECHANICAL DETAIL IS SHOWN BELOW



## TEST CONDITIONS

1. All measurements are made at 25°C.
2. Vee supply voltage is maintained at -5.2V DC.
3. All units are tested using a positive input pulse provided by a standard open emitter ECL 10,000 gate. The input and output utilize a 100 ohm pulldown resistor to -2V; the output is also loaded with one ECL 10,000 gate.
4. Input is ECL 10,000 square wave at 20% of output frequency.

## OPERATING SPECIFICATIONS

Supply Voltage . . . . .	- 5.2V ±5% to Vee (Can be operated on +5V to Vcc)
Supply Current; . . . . .	50ma typical
Logic 1 Input at 25°C.	
Voltage . . . . .	-.98V min.
Current . . . . .	530ua max.
Logic 0 Input at 25°C.	
Voltage . . . . .	- 1.63V max.
Current . . . . .	1ua min.
Logic 1 Output at 25°C. . . . .	-.96V min.
Logic 0 Output at 25°C. . . . .	- 1.65V max.
Operating temperature range: . . . . .	-30° to +85°C.
Storage temperature: . . . . .	-55° to +125°C.

## PART NUMBER TABLE

PART NUMBER	OUTPUT FREQUENCY	PART NUMBER	OUTPUT FREQUENCY
ECLDFMM-2	2Mhz	ECLDFMM-35	35Mhz
ECLDFMM-3	3Mhz	ECLDFMM-40	40Mhz
ECLDFMM-4	4Mhz	ECLDFMM-45	45Mhz
ECLDFMM-5	5Mhz	ECLDFMM-50	50Mhz
ECLDFMM-10	10Mhz	ECLDFMM-60	60Mhz
ECLDFMM-15	15Mhz	ECLDFMM-70	70Mhz
ECLDFMM-20	20Mhz	ECLDFMM-80	80Mhz
ECLDFMM-25	25Mhz	ECLDFMM-90	90Mhz
ECLDFMM-30	30Mhz	ECLDFMM-100	100Mhz

Special modules can be readily manufactured to provide customer specified output frequencies for specific applications.