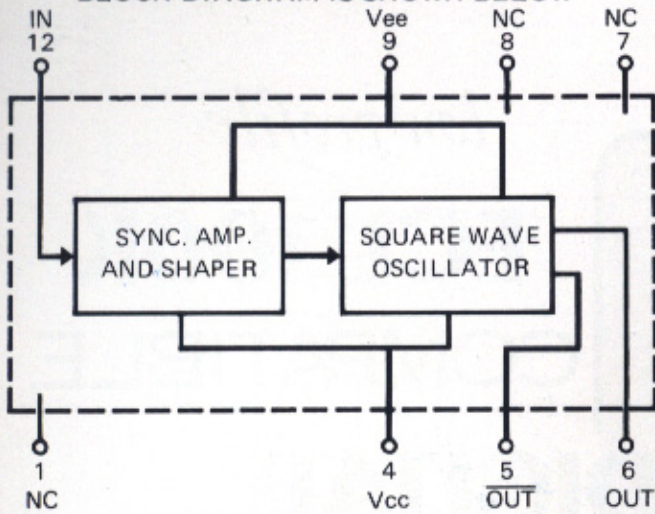
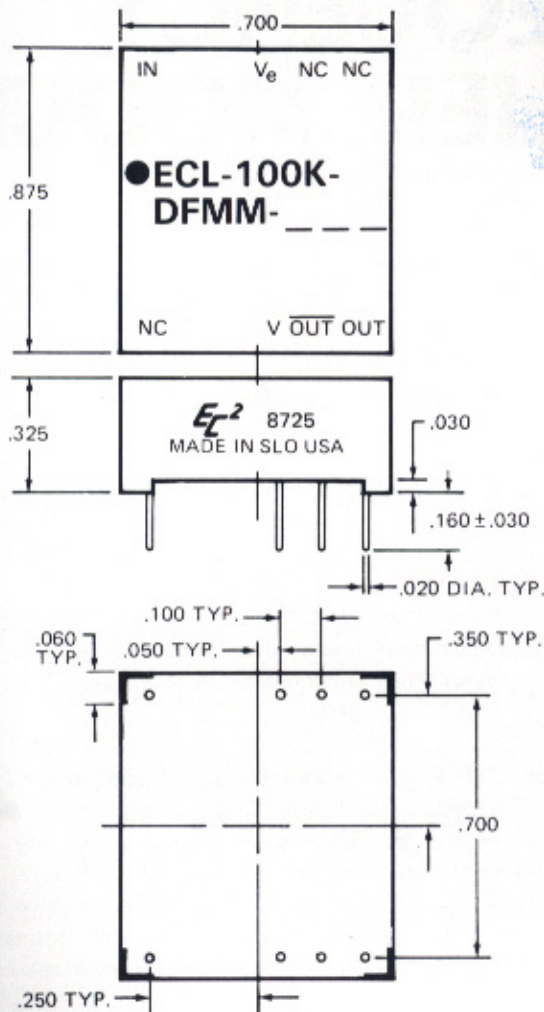


**BLOCK DIAGRAM IS SHOWN BELOW**



**MECHANICAL DETAIL IS SHOWN BELOW**



**OPERATING SPECIFICATIONS**

Supply voltage: . . . . . - 4.5V ± 5% to V<sub>ee</sub>

Supply current: . . . . . 102ma typical

Logic 1 input at 25°C.

Voltage . . . . . - .88V

Current . . . . . 700ua max.

Logic 0 input at 25°C.

Voltage . . . . . - 1.81V max.

Current . . . . . 1ua min.

Logic 1 Output at 25°C. . . . . - 1.025V min.

Logic 0 Output at 25°C. . . . . - 1.620V max.

Operating temperature range: . . . . . 0° to + 85°C.

Storage temperature: . . . . . - 55° to + 125°C.

**PART NUMBER TABLE**

Part Number	Output Frequency	Part Number	Output Frequency
ECL-100K-DFMM-50	50 Mhz	ECL-100K-DFMM-130	130 Mhz
ECL-100K-DFMM-55	55 Mhz	ECL-100K-DFMM-140	140 Mhz
ECL-100K-DFMM-60	60 Mhz	ECL-100K-DFMM-150	150 Mhz
ECL-100K-DFMM-65	65 Mhz	ECL-100K-DFMM-160	160 Mhz
ECL-100K-DFMM-70	70 Mhz	ECL-100K-DFMM-170	170 Mhz
ECL-100K-DFMM-75	75 Mhz	ECL-100K-DFMM-180	180 Mhz
ECL-100K-DFMM-80	80 Mhz	ECL-100K-DFMM-190	190 Mhz
ECL-100K-DFMM-85	85 Mhz	ECL-100K-DFMM-200	200 Mhz
ECL-100K-DFMM-90	90 Mhz	ECL-100K-DFMM-210	210 Mhz
ECL-100K-DFMM-95	95 Mhz	ECL-100K-DFMM-220	220 Mhz
ECL-100K-DFMM-100	100 Mhz	ECL-100K-DFMM-230	230 Mhz
ECL-100K-DFMM-110	110 Mhz	ECL-100K-DFMM-240	240 Mhz
ECL-100K-DFMM-120	120 Mhz	ECL-100K-DFMM-250	250 Mhz

**TEST CONDITIONS**

1. All measurements are made at 25°C.
2. V<sub>ee</sub> supply voltage is maintained at - 4.5V DC.
3. All units are tested using an input provided by a standard open emitter ECL 100K gate. The input and output utilize a 50 ohm pulldown resistor to - 2V; the output is also loaded with one ECL 100K gate.
4. Input is ECL 100K square wave at 20% of output frequency.

Special modules can be readily manufactured to provide customer specified output frequencies for specific applications.