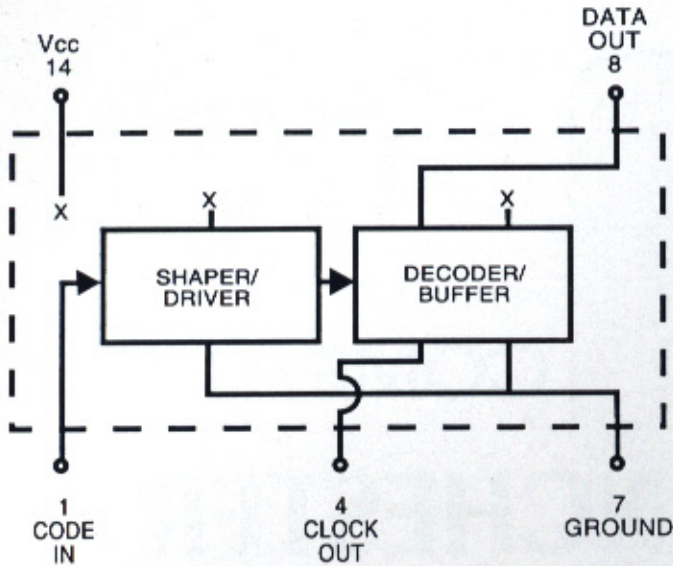


BLOCK DIAGRAM IS SHOWN BELOW



OPERATING SPECIFICATIONS

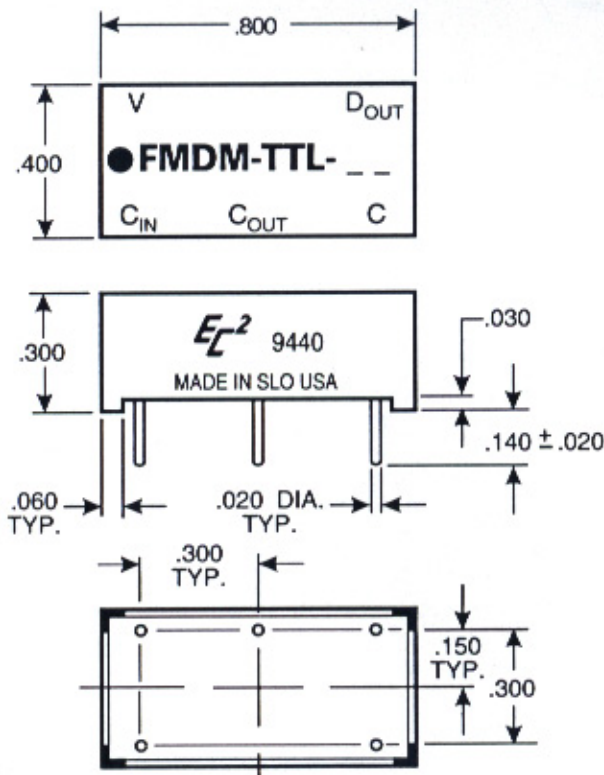
V_{CC} supply voltage: 4.75 to 5.25V DC
 V_{CC} supply current: 80mA typical

Logic 1 Input:
 Voltage 2V min.; V_{CC} max.
 Current 2.7V = 40uA max.
 5.5V = 2mA max.

Logic 0 Input:
 Voltage8V max.
 Current -1.2mA max.

Logic 1 Voltage out: 2.7V min.
 Logic 0 Voltage out:5V max.
 Operating temperature range: 0 to 70°C
 Storage temperature: -55 to +125°C.

MECHANICAL DETAIL IS SHOWN BELOW



PART NUMBER TABLE

Part Number	Output Frequency	Part Number	Output Frequency
FMDM-TTL-2	2.0 MHz	FMDM-TTL-12	12.0 MHz
FMDM-TTL-2.5	2.5 MHz	FMDM-TTL-13	13.0 MHz
FMDM-TTL-3	3.0 MHz	FMDM-TTL-14	14.0 MHz
FMDM-TTL-3.5	3.5 MHz	FMDM-TTL-15	15.0 MHz
FMDM-TTL-4	4.0 MHz	FMDM-TTL-16	16.0 MHz
FMDM-TTL-4.5	4.5 MHz	FMDM-TTL-17	17.0 MHz
FMDM-TTL-5	5.0 MHz	FMDM-TTL-18	18.0 MHz
FMDM-TTL-5.5	5.5 MHz	FMDM-TTL-19	19.0 MHz
FMDM-TTL-6	6.0 MHz	FMDM-TTL-20	20.0 MHz
FMDM-TTL-7	7.0 MHz	FMDM-TTL-21	21.0 MHz
FMDM-TTL-8	8.0 MHz	FMDM-TTL-22	22.0 MHz
FMDM-TTL-9	9.0 MHz	FMDM-TTL-23	23.0 MHz
FMDM-TTL-10	10.0 MHz	FMDM-TTL-24	24.0 MHz
FMDM-TTL-11	11.0 MHz	FMDM-TTL-25	25.0 MHz

TEST CONDITIONS

1. All measurements are made at 25°C.
2. V_{CC} supply voltage is maintained at 5.0V DC.
3. All units are tested using a FAST T²L toggle-type source and one FAST T²L load at the output being tested.
4. Input signal used is a Manchester data stream at the nominal bit rate and with no duty cycle skewing.

Special modules can be readily manufactured to improve tolerance and/or provide customer specified data rates for specific applications.