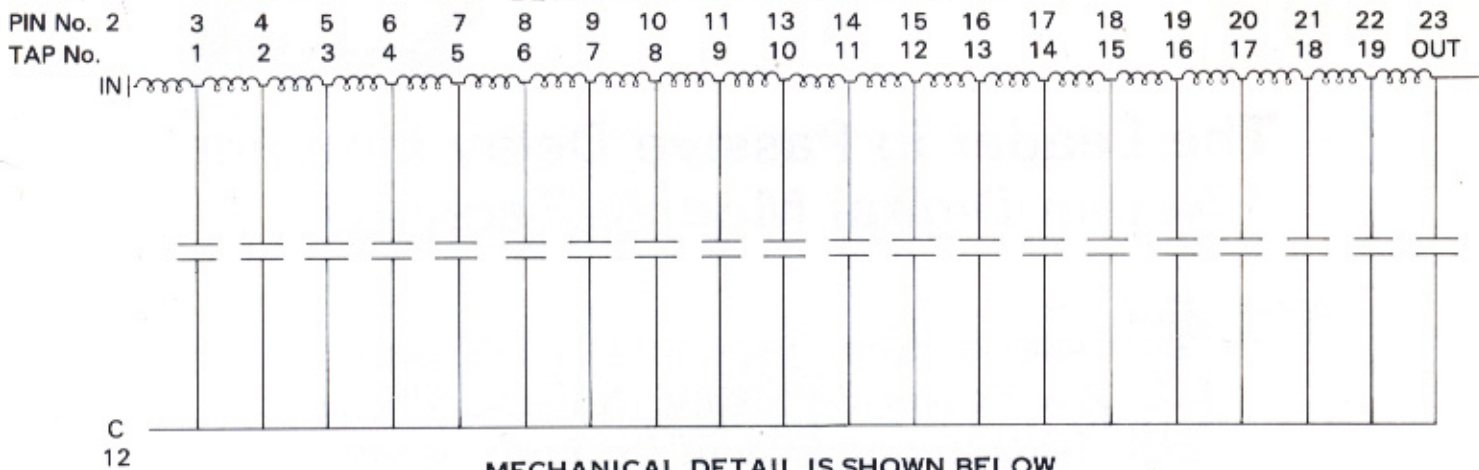
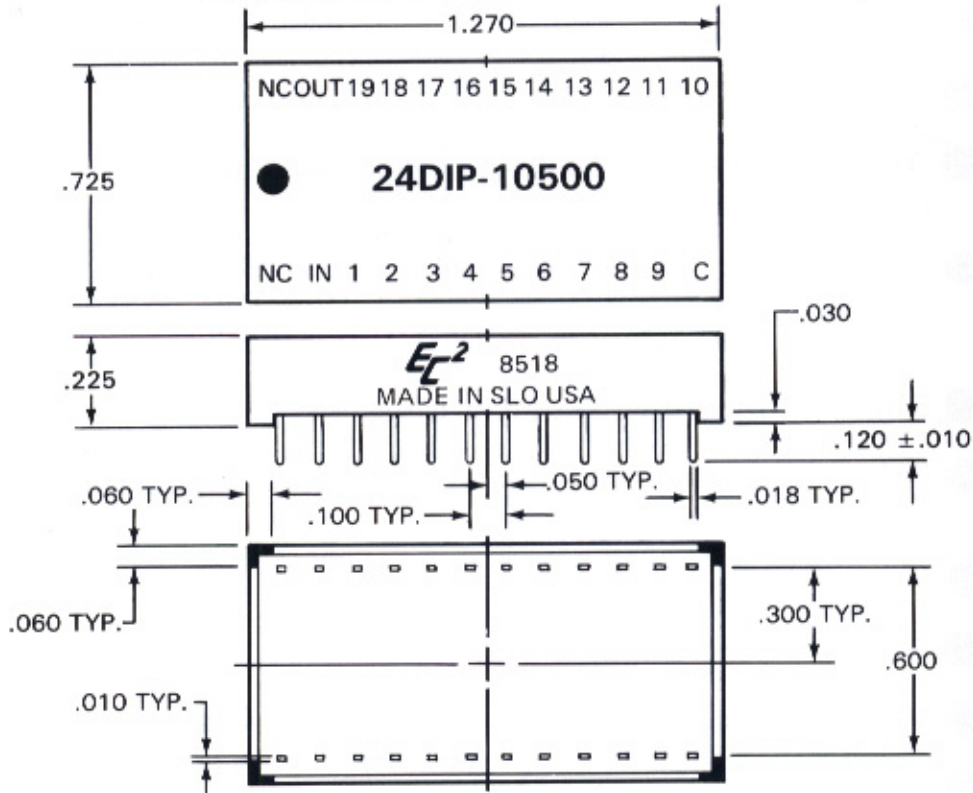


**BLOCK DIAGRAM IS SHOWN BELOW**



**MECHANICAL DETAIL IS SHOWN BELOW**



**TEST CONDITIONS**

1. All measurements are made at 25°C.
2. Test procedures in accordance with MIL-D-23859.

**OPERATING CHARACTERISTICS**

Total delay tolerance:	See tabulations
Tap delay tolerance:	See tabulations $\phi$
Rise time, maximum:	See tabulations
Impedance:	50, 100, 200, 350 and 500 ohms
Impedance tolerance:	± 10%
DC resistance, maximum:	See tabulations
Attenuation, maximum:	1.0 db
Distortion, maximum:	± 5%
Overshoot, maximum:	10%
Working voltage, maximum:	25V DC
Dielectric strength:	100V DC @ 50ua
Insulation resistance, minimum:	10,000 megohms @ 100V DC

$\phi$  Referenced from input of delay line.